

2022 IEEE International Conference on Current Development in Engineering and Technology (CCET)

Tech Session No. : TS-19

DATE: 24/12/2022, Time- 10:15-11:45AM

Venue: Seminar Hall, F-Block

Session Chair- Dr V Evelyn Brindha, Karunya Institute of Technology. Coimbatore

Session Co-Chair- Dr Jaya Parvathy, SSN College of Engineering, Kalavakkam, Tamil Nadu

Track V- Wireless Communication & Signal Processing

S.No	Time Slot	Paper ID	Paper Title	Author Names
1	10:15 - 10:30	35	Smart Electronic Arm Module Using Arduino Applications	Kishore KannaRavikumar
2	10:30 - 10:45	39	Performance Evaluation of Dual-Metal (DM) Dielectric-Pocket (DP) Nanotube MOSFETs at Extreme Temperature conditions	VaibhavPurwar
3	10:45 - 11:00	43	Automated Defective ECG Signal Detection Using MATLAB Applications	Kishore KannaRavikumar
4	11:00 - 11:15	286	Fuzzy Logic Based WSN with High Packet Success Rate and Security	NAGI VELPULA REDDY
5	11:15 - 11:30	297	Vertical-Architecture based Nanowire TFET using Gate Engineering Technique- Design and Performance analysis	Anjana Bhardwaj
6	11:30 - 11:45	425	Studying the frequency behavior of the interdigital capacitive sensor during water level measurement	Prem Ranjan

General Guideline:

- 1. PPT should contain major contribution since the presentation time will be 13 minutes and 2 min for question & answer.**
- 2. The link for online presentation will be shared 1 days prior to conference.**

Expert 1 - Dr V Evelyn Brindha, Karunya Institute of Technology. Coimbatore

Expert 2 - Dr Jaya Parvathy, SSN College of Engineering, Kalavakkam, Tamil Nadu

2022 IEEE International Conference on Current Development in Engineering and Technology (CCET)

Tech Session No. : TS-20

DATE: 24/12/2022, Time- 12:00-1:30 PM

Venue: Seminar Hall, F-Block

Session Chair- Dr V Evelyn Brindha, Karunya Institute of Technology, Coimbatore

Session Co-Chair- Dr Jaya Parvathy, SSN College of Engineering, Kalavakkam, Tamil Nadu

Track V- Wireless Communication & Signal Processing

S.No .	Time Slot	Paper ID	Paper Title	Author Names
1	12:00 - 12:15	14	Design of a modified Current Starved Inverter Based Ring Oscillator for Switched Capacitor Circuit	S K Dash
2	12:15 - 12:30	172	DESIGN OF HIGH PERFORMANCE PARALLEL MULTIPLICATION USING FPGA	Jenila R
3	12:30 - 12:45	227	A Compact Slotted Antenna with Two Parallel Stubs of Vertical Open-ended in Nature for Application in Wireless Communication	Jyoti Ranjan Panda
4	12:45 - 01:00	349	Intermittent Fault Diagnosis in Sensors using Artificial Neural Network: A Comparative Study	ARUNA KUMAR MISHRA
5	01:00 - 01:15	921	Optimization of Diabetes Risk Prediction using Machine Learning	Navneet Kaur

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